International Research Universities Network

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IEEE

The Faculty of Information Technology, Pázmány Péter Catholic University, Budapest under the patronage of IEEE Hungary Section is organizing an IRUN Workshop with the title „**Theory and Practice of Kiloprocessor Architectures**" in 5-7 November, 2012, in the building of the Faculty, Budapest, Práter u. 50/a, Hungary

Deadline for abstracts: October 1, 2012

Registration deadline: October 26, 2012

Deadline for submitting lectures: October 29.

homepage:http://www.en.itk.ppke.hu/education/workshop_november_5-7./

Registration requests and the indication of presentation type are expected at the following address:

irunbud2012@itk.ppke.hu

HUNGARY'S RENEWAL

The projects are supported by the European Union.

Since the workshop is organized as an IRUN event, PhD candidates and young scientists are highly welcome to participate.

There is **no registration fee** due to a generous support by an EU Grant (TÁMOP-4.2.2/B-10/1-2010-0014) but registration is necessary.

For participants who plan to make a presentation (oral presentation or poster) the deadline for abstracts: October 1.

Abstracts are reviewed and accepted for presentation by : October 5.

Deadline for submitting lectures: **October 29**.

Language: The official language of the conference is English.

Scientific Committee:
Prof. Tamás Roska
Dr. Ákos Zarándy
Prof. Péter Arató
Prof. Ferenc Friedler

The program:
Monday, 5th November, 2012

8:45	Opening remarks by the Local Organizing Committe
9:00	CHAIRMAN: Prof. Péter Arató
	Plenary Lecture :
	Prof. Tamás Roska (PPCU Budapest): Physical and Virtual Cellular Machines for Nanoscale Architectures
9:45	Coffee break
10:15	Plenary Lecture Prof. Ronald Tetzlaff (Technische Universitaet Dresden, Germany)
11:00-12.30	Presentations by participants
12:30-14:00	Lunch break
14:00	CHAIRMAN: Prof. Péter Szolgay
	Plenary lecture Prof. Vedat Tavsanoglu (Yildiz Technical University, Istanbul, Turkey):

The Mathematics and FPGA Implementation of a Class of Real-Time CNN Processors

15:00	Presentations by participants, Poster session
16:00	Coffee break
18:00	Welcome Reception

Tuesday, 6th November, 2012

9:00	CHAIRMAN: Prof. Tamás Roska
	Plenary lecture dr. Müstak Yalcin (Istanbul Technical University, Turkey)
10:00	Prof. Péter Arató (Technical University, Budapest): High-Level Synthesis of a Task-Oriented Multiprocessing Structure
10:45	Coffee break
11:00-12.30	Presentations by participants
12:30-14:00	Lunch break
14:00	CHAIRMAN: Dr. Ákos Zarándy
	Plenary lecture dr. Xavier Vilasis-Cardona (La Salle, Universitat Ramon Llull, Barcelona, Spain): Multicore trends in high energy physics applications
15:00	Presentations by participants, Poster session
15:30	Coffee break
18:00	Social event: 1 hour sightseeing boat tour on the Danube
19:00	Invited dinner

Wednesday, 7th November, 2012

9:00	CHAIRMAN: Prof. Vedat Tavsanoglu
	Plenary lecture, Prof. Ferenc Friedler (Pannon University, Veszprém, Hungary)
10:00	Plenary lecture, Prof Alexandru Gacsádi (University of Oradea, Romania): Variational Computing Based Image Processing Methods by using Cellular Neural Networks
10:45	Coffee break
11:00-12:30	Presentations by participants
12:30-14:00	Lunch break
14:00	Plenary lecture, Prof. Péter Szolgay (PPCU, Budapest): Toward Mega-Core Processor Architectures
15:00	Lab visits
16:00	Closing remarks

We will do our best to let you experience valuable time interacting with colleagues from IRUN Partner Universities and explore new research strategies revealed by distinguished scientists of this field!

Hope to see many of you in Budapest!   The Organizers